



TECHNICAL STAFF REQUIRED

The services of the following staff are required in the **projects mentioned below** for a period of two years in the Department of Electrical Engineering, UET Peshawar. Those who fulfill the required criteria, may apply on plain paper alongwith CV, CNIC, one recent photograph, attested copies of Educational and Experience Certificates to the undersigned by, **02-02-2012**. The requisite qualification, experience and responsibilities for the positions are given below:

“DESIGN AND IMPLEMENTATION OF PROTOTYPE FOR A SECURE BILLING FRAMEWORK WITH REAL DETECTION OF MALICIOUS END NODE CONNECTION USING WIRELESS SENSOR NETWORK TO CURB ELECTRICITY THEFT”

| S.No. | Name of Posts | Minimum Qualifications and Experience | Responsibilities |
|-------|----------------------------|--|--|
| 1. | Research Assistant | a. B.Sc in Electrical / Electronics / Power Engineering from an HEC recognized University / Institution. b. Expertise in C/C++, Assembly Language, Microcontrollers, Hardware/Embedded System Designs and Interfacing, FPGA/DSP System designs, HDL based languages. | Hardware Design and Implementation |
| 2. | Research Assistant | a. B.Sc in Software / Computer / Electrical Engineering / Computer Science from an HEC recognized University / Institution b. Expertise in C/C++, Assembly Language, Object Oriented Programming, Database Design (MS SQL Server / Oracle / MySQL) | Software Development and Database Design |
| 3. | Research Assistant | a. B.Sc Electrical Engineering / Communication, Telecommunication on Engineering from an HEC recognized University / Institution. b. Expertise in OPNET, NS-2 Assembly Language, Object Oriented Programming, Matlab | Communication Network, Protocol and Data Scheduling |
| 4. | Research Associates | a. M.Sc Electrical / Electronics / Computer System / Power Engineering from an HEC recognized University / Institution. b. Minimum 2 years expertise in C/C++, Assembly Language, Microcontrollers, Embedded System Design, HDL Based Languages, FPGAs / DSP based systems | Embedded system design, Digital System design, Transceiver based system design, Integration of RF module with digital signal processors and microcontrollers, Interfacing the RF module with Laptop or Computer. |
| 5. | Research Associates | a. M.Sc Electrical / Electronics / Computer System / Software / Communication Engineering from an HEC recognized University / Institution. b. Minimum 2 years expertise in C/C++, Assembly Language, Object Oriented Programming, Database Design (MS SQL Server / Oracle / MySQL), OPNET, NS-2 | Relational database design, Implementation of Software and connecting it with database, Implementing a web browser for updates and information, 3D Modeling of system parameters and Graphical interface development for real time monitoring and connectivity, Communication network and protocol design, Data scheduling Algorithms. |
| 6. | Development Staff | B.Sc (In Progress) Electrical / Computer Systems / Electronics Engineering. | Nil |
| 7. | Development Staff | B.Sc (In progress) Electrical / Computer Systems / Software Engineering. | Assist the Team Leads and Researchers in Hardware and Software development. |

“A SECURE AND INTELLIGENT TRANSPORTATION SYSTEM USING WIRELESS SENSORS FOR CONGESTION CONTROL”

| S.No. | Name of Posts | Minimum Qualifications and Experience | Responsibilities |
|-------|----------------------------|---|---|
| 1. | Team Lead | a. M.Sc Electrical / Electronics / Telecom / Software Engineering / Computer Science from an HEC recognized University / Institution. b. Minimum 2 years expertise in C/C++, Assembly Language, Data Structures and Algorithms, Database Design (MS SQL Server / Oracle/MySQL) | Software + Hardware Development |
| 2. | Research Associates | a. M.Sc Electrical / Electronics / Computer System Engineering from an HEC recognized University / Institution. b. Minimum 2 years expertise in C/C++, Assembly Language, 8051 Microcontrollers, Embedded System Design. | Wireless Sensor Design/Development and Configuration. |
| 3. | Research Associates | a. M.Sc Electrical / Electronics / Computer System / Software Engineering from an HEC recognized University / Institution. b. Minimum 2 years expertise in C/C++, Assembly Language, Data Structures and Algorithms, Database Design (MS SQL Server / Oracle / MySQL), TCP/IP. | Implementation and Testing of Queuing Theory Algorithms and Multichannel Assignment techniques. |
| 4. | Development Staff | B.Sc (In progress) Electrical / Computer Systems / Electronics Engineering | Assist the Team Leads and Researchers in Hardware and Software development. |
| 5. | Development Staff | B.Sc (In progress) Electrical / Computer Systems / Software Engineering. | Assist the Team Leads and Researchers in Hardware and Software development. |

Registrar

University of Engineering & Technology, Peshawar

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